

## **Amendments to the Claims**

### **Listing of Claims:**

Claims 1-8 (canceled).

Claim 9 (new): A circuit configuration for generating a control signal for an engine control unit controlling at least one fuel injector of an internal combustion engine, wherein operating parameters of at least one of the internal combustion engine and the fuel injector are used for generating a modification signal input to the circuit configuration for an operational variation of a course of the control signal, the circuit configuration comprising:

a counter device connected to receive a predefined clock signal for providing a time-dependent digital counter signal;

a memory device connected to receive the digital counter signal, said memory device being configuration to store a series of digital control signal values and to successively issue individual control signal values from the series of control signal values in dependence on the counter signal; and

a digital-to-analog converter for converting the digital control signal values issued from said memory device into analog control signals for the engine control unit.

Claim 10 (new): The circuit configuration according to claim 9, which comprises a voltage-controlled oscillator connected to receive the modification signal as a time

scaling signal and generating the clock signal with the selected frequency.

Claim 11 (new): The circuit configuration according to claim 9, which comprises an oscillator with a fixed oscillation frequency and a divider connected to an output of said oscillator for generating the clock signal with the selected frequency, said divider having a division ratio determined by the modification signal input to said divider as a time scaling signal.

Claim 12 (new): The circuit configuration according to claim 9, wherein said memory device stores a series of at least 30 control signal values forming the series of control signal values.

Claim 13 (new): The circuit configuration according to claim 9, wherein said memory device stores a series of at least 50 control signal values forming the series of control signal values.

Claim 14 (new): The circuit configuration according to claim 9, wherein the series of control signal values stored in said memory device approaches a continuous function.

Claim 15 (new): The circuit configuration according to claim 9, wherein the digital control signal values are provided with a resolution of at least 8 bits.

Claim 16 (new): The circuit configuration according to claim 9, wherein said

memory device is a read-only memory.

Claim 17 (new): A method of generating a control signal for an engine control unit controlling at least one fuel injector of an internal combustion engine, wherein operating parameters of the internal combustion engine and/or of the fuel injector are used for generating a modification signal for an operational variation of a course of the control signal, the method which comprises:

counting a predefined clock signal and providing a time-dependent digital counter signal, wherein the clock signal is predefined with a frequency that is set in accordance with the modification signal;

successively issuing individual digital control signal values in accordance with the counter signal from a previously stored series of control signal value; and

converting the issued digital control signal values into an analog control signal for the engine control unit, and taking the modification signal into account as an amplitude scaling signal in converting the digital control signal values into the analog control signal.